

	Search Text
1	library with (component circuit functional cell)
2	library with (component element circuit functional cell) and (manufactur\$4 fabricat\$4 process\$4) same (elements component circuit functional cell ) same semiconductor
3	library with (component element circuit functional cell) and (manufactur\$4 fabricat\$4 process\$4) same (elements component circuit functional cell ) same semiconductor and cost\$4 with (function constrain\$4 budget limit)
4	library with (component element circuit functional cell) and (manufactur\$4 fabricat\$4 process\$4) same (elements component circuit functional cell ) same semiconductor and cost\$4 with (function constrain\$4 budget limit) and (trade adj off tradeoff)
5	library with (component element circuit functional cell) and (manufactur\$4 fabricat\$4 process\$4) same (elements component circuit functional cell ) same semiconductor and cost\$4 with (function constrain\$4 budget limit) and (trade adj off tradeoff) and "716"/\$.ccls.
6	cost\$4 with (function constrain\$4 budget limit) and library and layout\$4 and (floor\$6)".
7	cost\$4 with (performance function constrain\$4 budget limit) and library with (elements component circuit functional cell) and (layout\$4 floor\$4) and (delay same (interconnect\$5 connect\$4 wire line path conductor))
8	cost\$4 with (performance function constrain\$4 budget limit) and library with (elements component circuit functional cell) and (layout\$4 floor\$4) and (delay same (interconnect\$5 connect\$4 wire line path conductor)) and (tradeoff trade adj off)
9	cost\$4 with (performance function constrain\$4 budget limit) and library with (elements component circuit functional cell) and (layout\$4 floor\$4) and (delay same (interconnect\$5 connect\$4 wire line path conductor)) and (tradeoff trade adj off) and (manufactur\$4 fabricat\$4 process\$4) with (element component circuit hardware device)
10	cost\$4 with (optim\$6 solution ) and library with (elements component circuit functional cell) and (layout\$4 floor\$4 ) and (delay same (interconnect\$5 connect\$4 wire line path conductor)) and (tradeoff trade adj off) and (manufactur\$4 fabricat\$4 process\$4) with ( technology element component circuit hardware device)
11	cost\$4 with (performance function constrain\$4 budget limit) and library with (elements component circuit functional cell) and (layout\$4 floor\$4) and (delay same (interconnect\$5 connect\$4 wire line path conductor)) and (tradeoff trade adj off) and cost\$4 with (process\$4 technology)
12	cost\$4 with (design manufactur\$4 fabricat\$4 ) and library with (elements component circuit functional cell) and (delay same (interconnect\$5 connect\$4 wire line path conductor)) and (tradeoff trade adj off) and cost\$4 with (process\$4 technology) and (floor\$7 layout delay with (interconnect\$4 connect\$4 wir\$4 ))